

FIG. 1

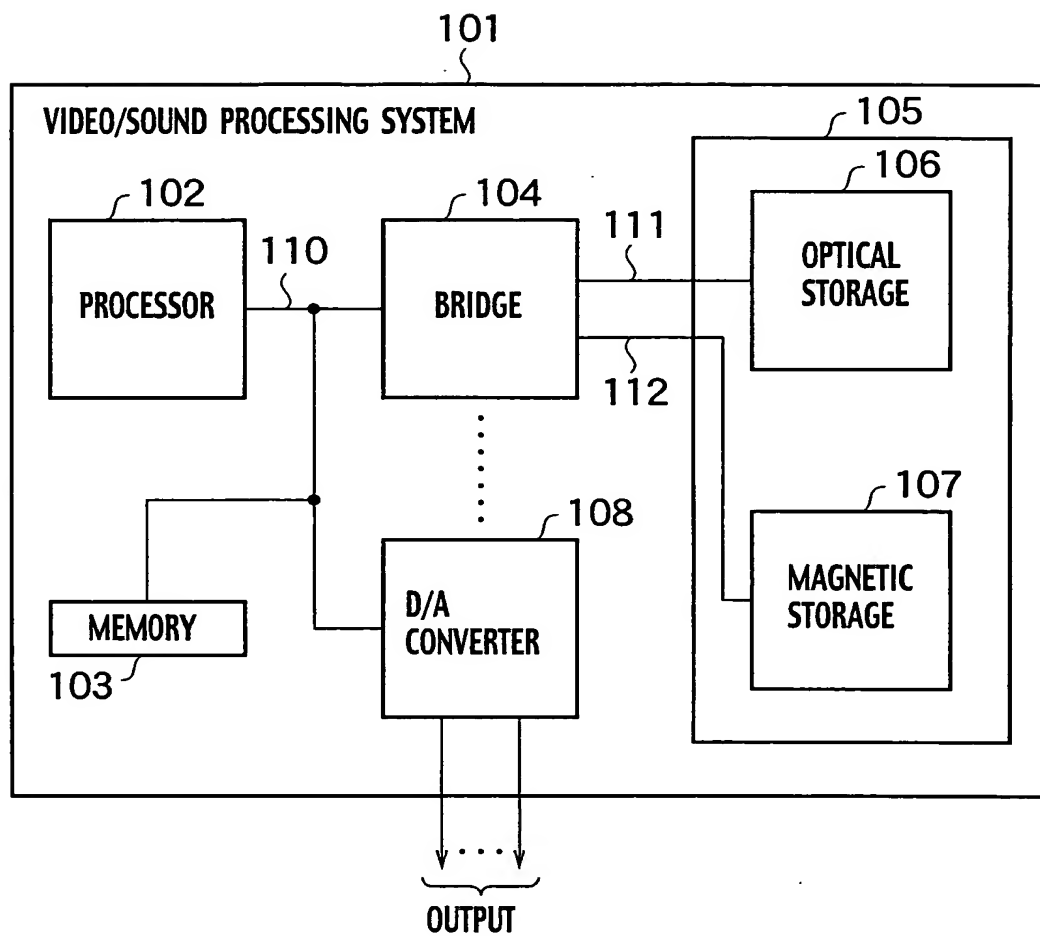


FIG. 2

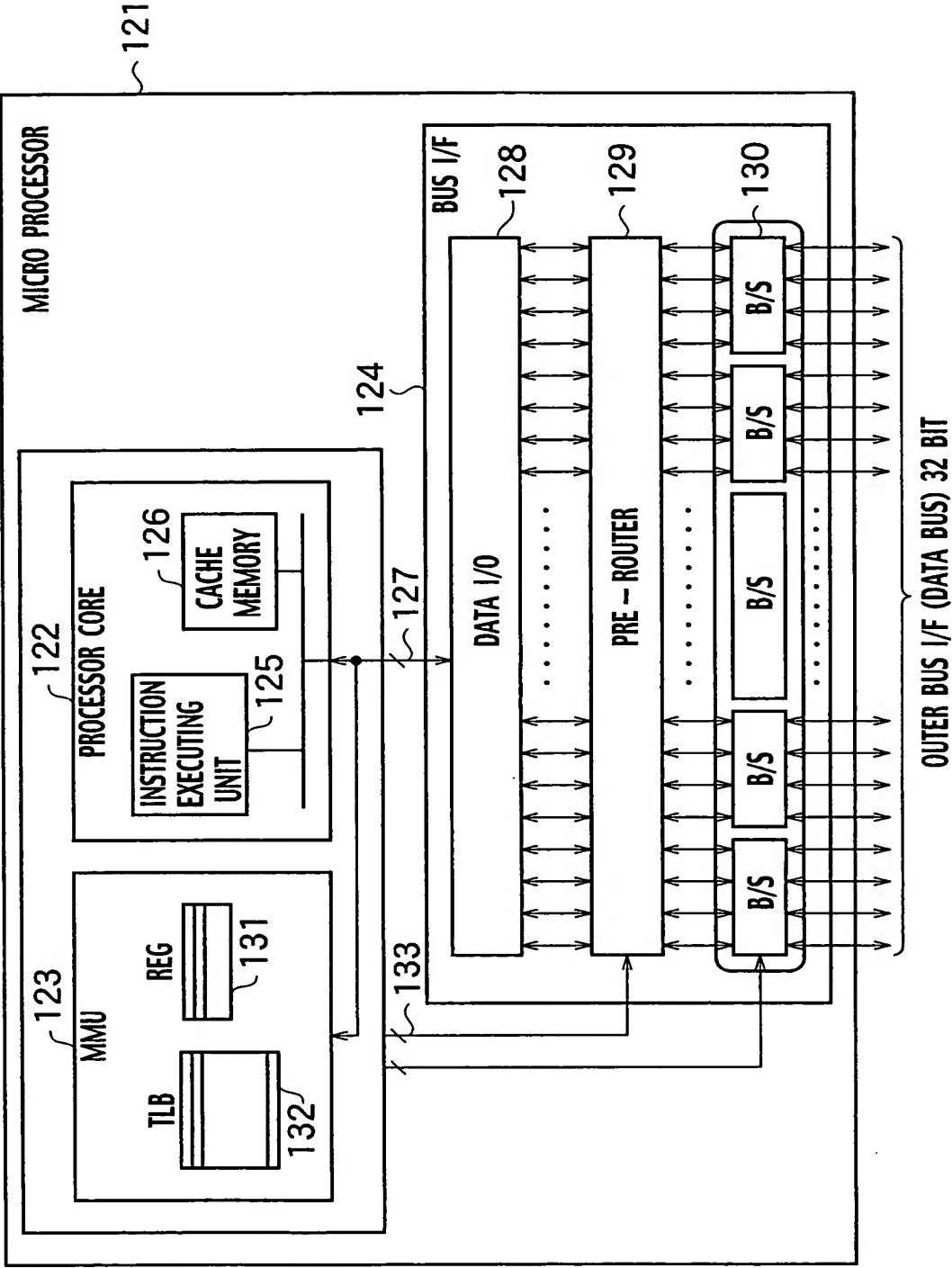


FIG. 3

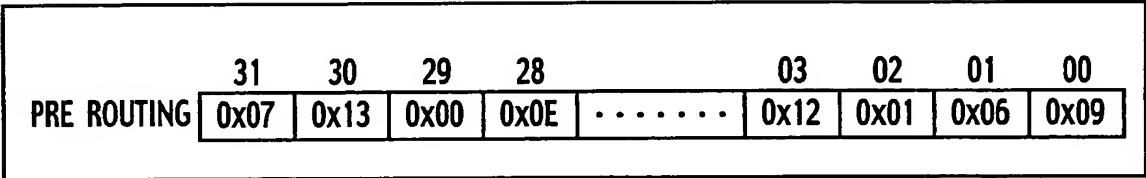
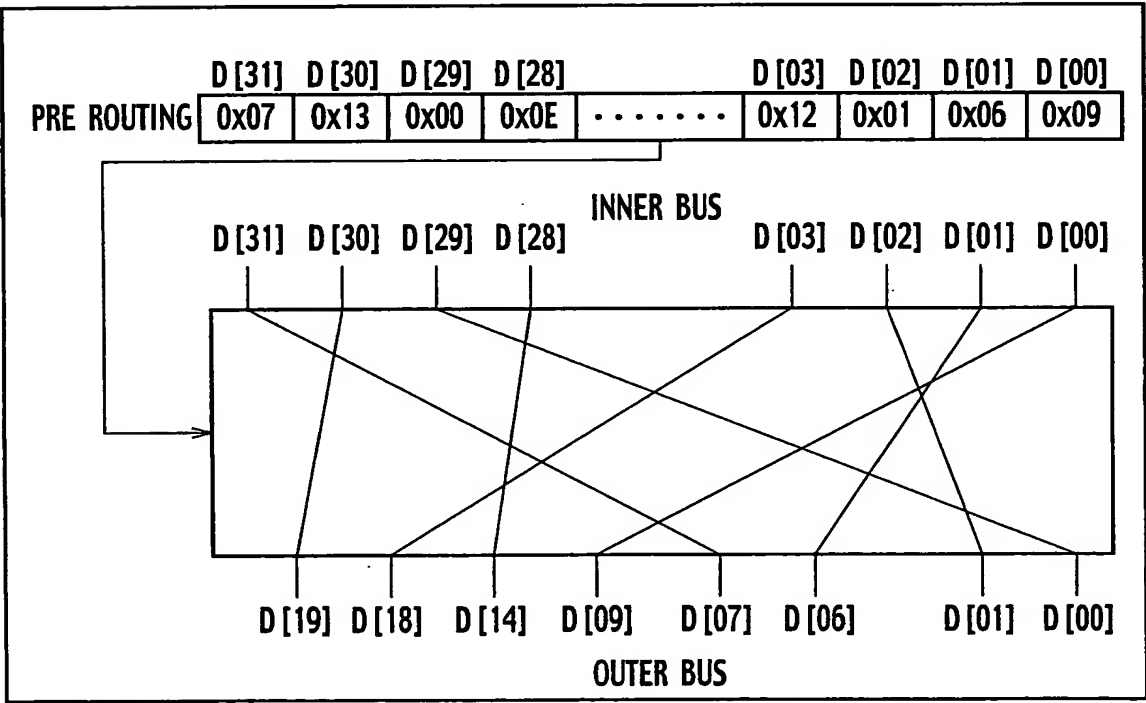


FIG. 4



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FIG. 5

BUS SWITCH CONTROL
INFORMATION

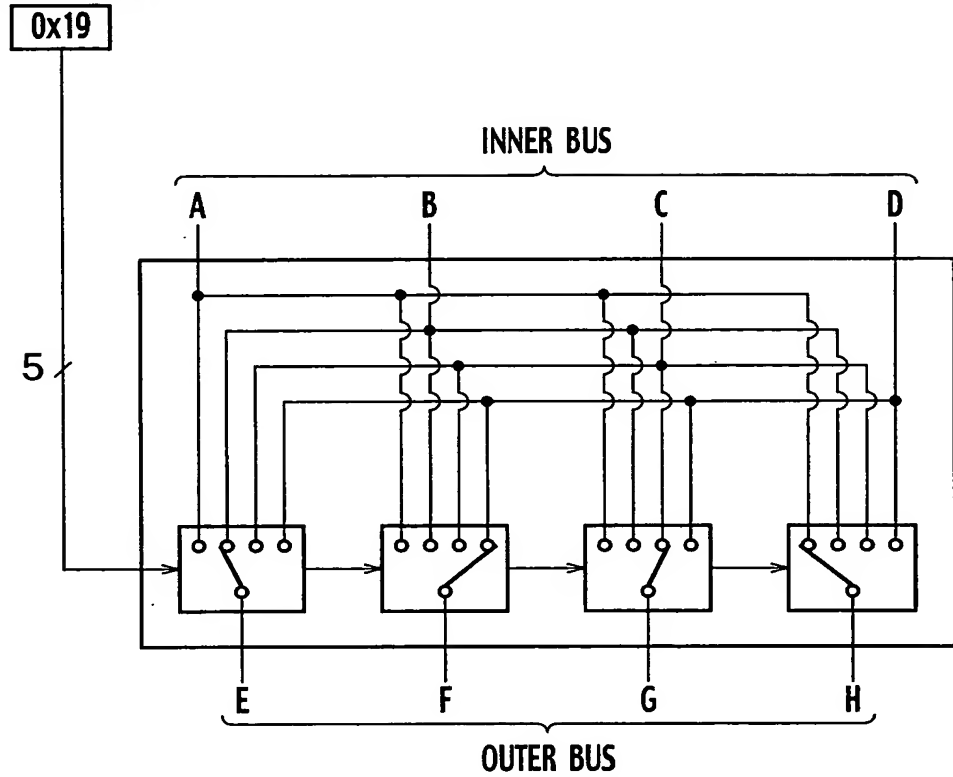
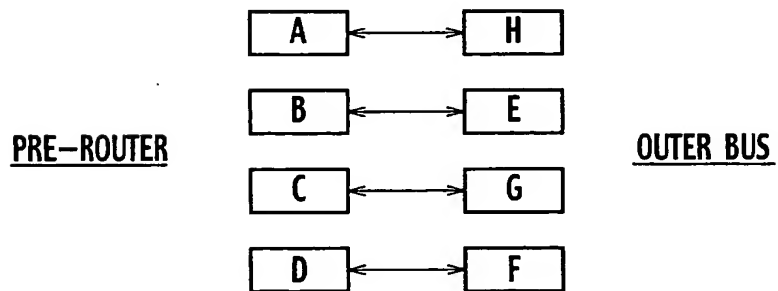


FIG. 6

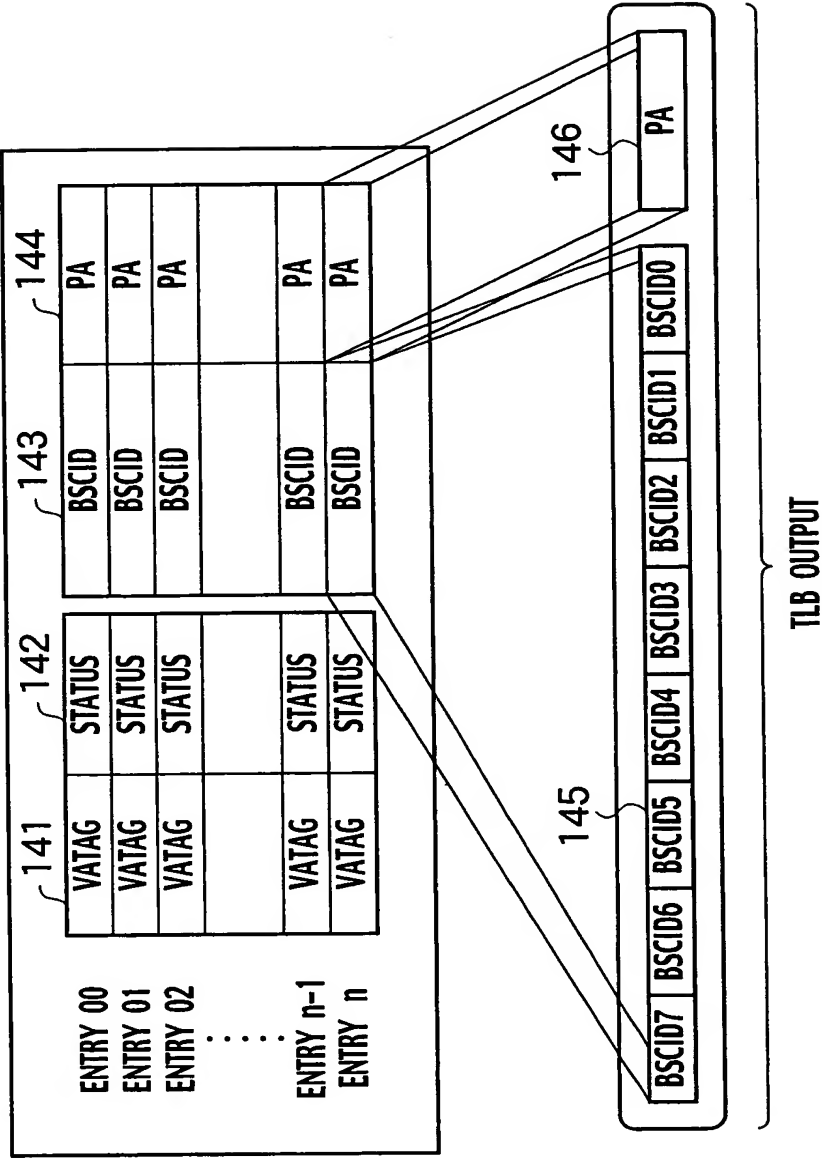


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FIG. 7

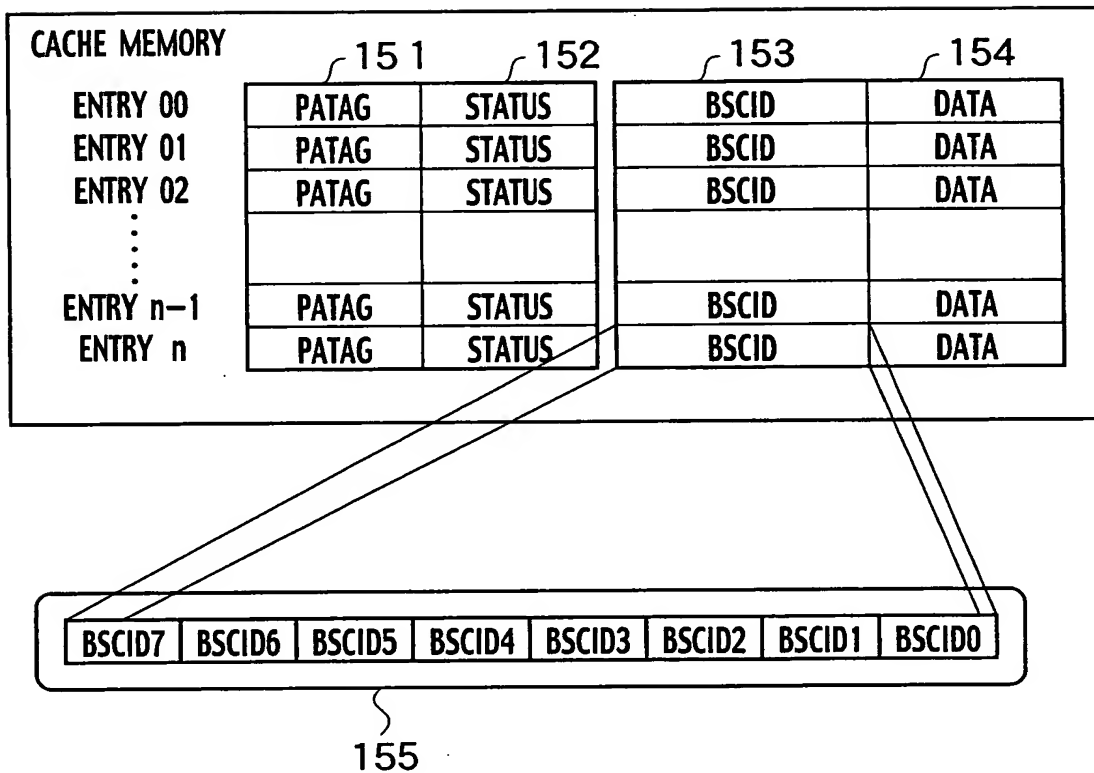
BUS SWITCH CONTROL						BUS SWITCH			
HEXADECIMAL	BINARY					A	B	C	D
0x00	0	0	0	0	0	E	F	G	H
0x01	0	0	0	0	1	E	F	H	G
0x02	0	0	0	1	0	E	G	F	H
0x03	0	0	0	1	1	E	G	H	F
0x04	0	0	1	0	0	E	H	F	G
0x05	0	0	1	0	1	E	H	G	F
0x06	0	0	1	1	0	—	—	—	—
0x07	0	0	1	1	1	—	—	—	—
0x08	0	1	0	0	0	F	E	G	H
0x09	0	1	0	0	1	F	E	H	G
0x0A	0	1	0	1	0	F	G	E	H
0x0B	0	1	0	1	1	F	G	H	E
0x0C	0	1	0	1	0	F	H	E	G
0x0D	0	1	0	1	1	F	H	G	E
0x0E	0	1	1	1	0	—	—	—	—
0x0F	0	1	1	1	1	—	—	—	—
0x10	1	0	0	0	0	G	E	F	H
0x11	1	0	0	0	1	G	E	H	F
0x12	1	0	0	1	0	G	F	E	H
0x13	1	0	0	1	1	G	F	H	E
0x14	1	0	0	1	0	G	H	E	F
0x15	1	0	0	1	1	G	H	F	E
0x16	1	0	1	1	0	—	—	—	—
0x17	1	0	1	1	1	—	—	—	—
0x18	1	1	0	0	0	H	E	F	G
0x19	1	1	0	0	1	H	E	G	F
0x1A	1	1	0	1	0	H	F	E	G
0x1B	1	1	0	1	1	H	F	G	E
0x1C	1	1	0	1	0	H	G	E	F
0x1D	1	1	0	1	1	H	G	F	E
0x1E	1	1	1	1	0	—	—	—	—
0x1F	1	1	1	1	1	—	—	—	—

FIG. 8



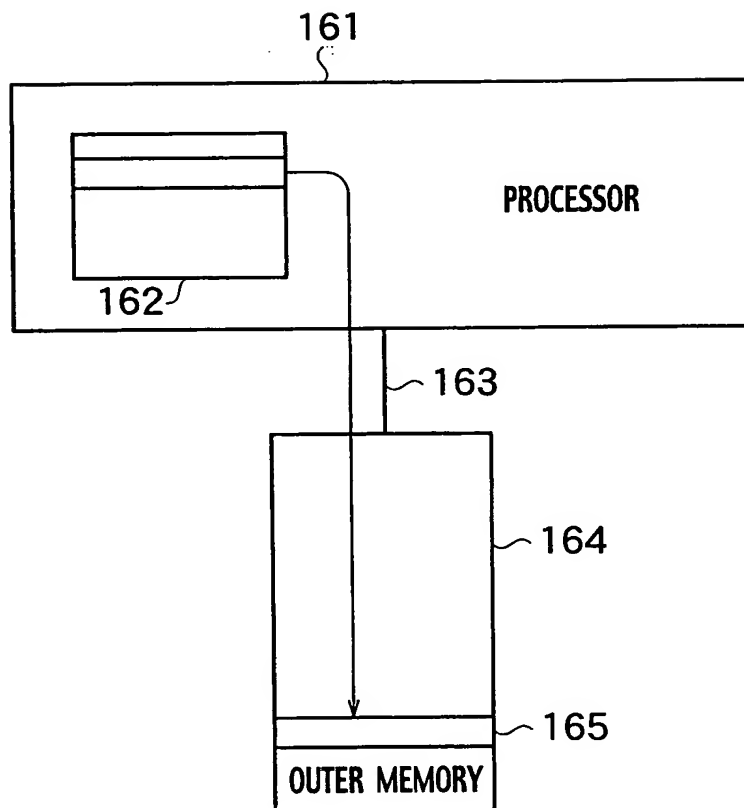
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FIG. 9



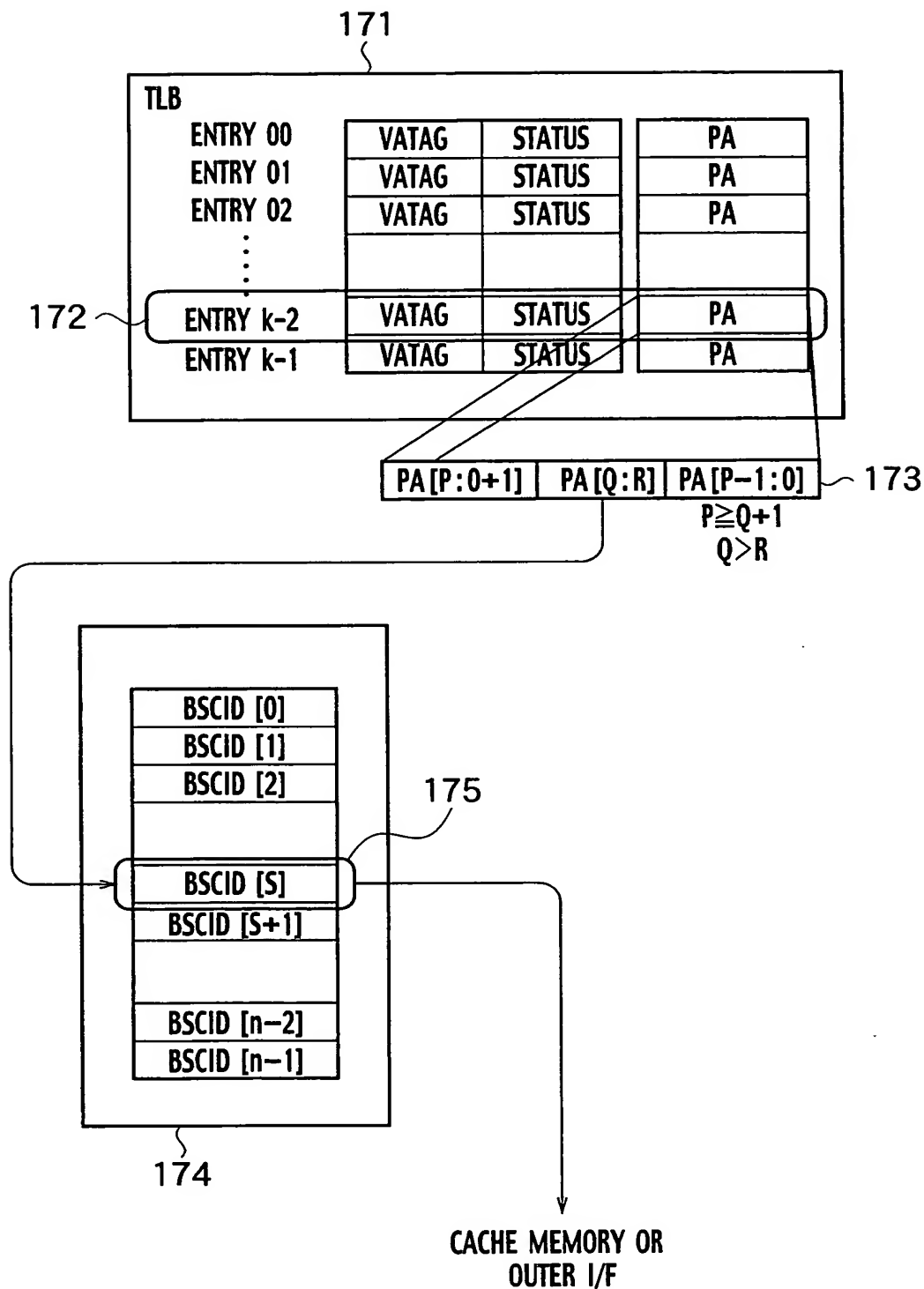
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FIG. 10



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FIG. 11



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FIG. 12

